

The listing of claims will replace all prior versions, and listings, of claims in the application:

**Listing of Claims:**

1. (Original) A process for producing a semiconductor device, comprising:  
forming a first semiconductor film having an amorphous structure;  
adding an element for promoting crystallization to the first semiconductor film having the amorphous structure;  
conducting a first heat treatment to form a first semiconductor film having a crystal structure;  
forming a second semiconductor film containing a rare gas element over the first semiconductor film having the crystal structure;  
conducting a second heat treatment to segregate the element for promoting crystallization into the second semiconductor film; and  
removing the second semiconductor film.
2. (Original) A process according to claim 1, wherein the second semiconductor film is formed by sputtering.
3. (Original) A process according to claim 1, wherein the second semiconductor film is formed by plasma CVD.
4. (Original) A process according to claim 1, wherein the first heat treatment is conducted by radiation from one or more selected from a halogen lamp, a metal halide lamp, a xenon arc lamp, a carbon arc lamp, a high-pressure sodium lamp, or a high-pressure mercury lamp.

5. (Original) A process according to claim 1, wherein the first heat treatment is conducted by furnace annealing using an electrical heating furnace.

6. (Original) A process according to claim 1, wherein the second heat treatment is conducted by radiation from one or more selected from a halogen lamp, a metal halide lamp, a xenon arc lamp, a carbon arc lamp, a high-pressure sodium lamp, or a high-pressure mercury lamp.

7. (Original) A process according to claim 1, wherein the second heat treatment is conducted by furnace annealing using an electrical heating furnace.

8. (Original) A process according to claim 1, wherein the rare gas element is one or more selected from He, Ne, Ar, Kr and Xe.

9. (Currently Amended) A process according to claim 1, wherein the element for promoting crystallization is one or more selected from Fe, Ni, Co[[.]], Ru, Rh, Pd, Os, Ir, Pt, Cu and Au.

10. (Currently Amended) A process for producing a semiconductor device, comprising:

forming a first semiconductor film having an amorphous structure;

adding an element for promoting crystallization to the first semiconductor film having the amorphous structure;

conducting a first heat treatment to form a first semiconductor film having a crystal structure;

forming a barrier layer over a surface of the first semiconductor film having the ~~amorphous~~ crystal structure;

forming a second semiconductor film containing a rare gas element over the barrier layer,

conducting a second heat treatment to segregate the element for promoting crystallization into the second semiconductor film, and

removing the second semiconductor film.

11. (Original) A process according to claim 10, wherein the second semiconductor film is formed by sputtering.

12. (Original) A process according to claim 10, wherein the second semiconductor film is formed by plasma CVD.

13. (Original) A process according to claim 10, wherein the barrier layer comprises a chemical oxide film formed using ozone water.

14. (Currently Amended) A process according to claim 10, wherein the barrier layer is formed by oxidizing the surface of the first semiconductor film having the ~~amorphous~~ crystal structure by plasma treatment.

15. (Currently Amended) A process according to claim 10, wherein the barrier layer is formed by oxidizing the surface of the first semiconductor film having the ~~amorphous~~ crystal structure with ozone generated by radiation of ultraviolet rays in an atmosphere containing oxygen.

16. (Original) A process according to claim 10, wherein the first heat treatment is conducted by radiation from one or more selected from a halogen lamp, a metal halide lamp, a xenon arc lamp, a carbon arc lamp, a high-pressure sodium lamp, or a high-pressure mercury lamp.

17. (Original) A process according to claim 10, wherein the first heat treatment is conducted by furnace annealing using an electrical heating furnace.

18. (Original) A process according to claim 10, wherein the second heat treatment is conducted by radiation from one or more selected from a halogen lamp, a metal halide lamp, a xenon arc lamp, a carbon arc lamp, a high-pressure sodium lamp, or a high-pressure mercury lamp.

19. (Original) A process according to claim 10, wherein the second heat treatment is conducted by furnace annealing using an electrical heating furnace.

20. (Original) A process according to claim 10, wherein the rare gas element is one or more selected from He, Ne, Ar, Kr and Xe.

21. (Currently Amended) A process according to claim 10, wherein the element for promoting crystallization is one or more selected from Fe, Ni, Co[[.]], Ru, Rh, Pd, Os, Ir, Pt, Cu and Au.

22. (Original) A process for producing a semiconductor device, comprising:  
forming a first semiconductor film having an amorphous structure;  
adding an element for promoting crystallization to the first semiconductor film having the amorphous structure;  
conducting a first heat treatment to form a first semiconductor film having a crystal structure;  
forming a second semiconductor film over the first semiconductor film having the crystal structure;  
adding a rare gas element to the second semiconductor film;

conducting a second heat treatment to segregate the element for promoting crystallization into the second semiconductor film, and  
removing the second semiconductor film.

23. (Currently Amended) A process according to claim 22, wherein the rare gas element is added by any one of ion implantation and [[or]] ion doping.

24. (Original) A process according to claim 22, wherein the first heat treatment is conducted by radiation from one or more selected from a halogen lamp, a metal halide lamp, a xenon arc lamp, a carbon arc lamp, a high-pressure sodium lamp, or a high-pressure mercury lamp.

25. (Original) A process according to claim 22, wherein the first heat treatment is conducted by furnace annealing using an electrical heating furnace.

26. (Original) A process according to claim 22, wherein the second heat treatment is conducted by radiation from one or more selected from a halogen lamp, a metal halide lamp, a xenon arc lamp, a carbon arc lamp, a high-pressure sodium lamp, or a high-pressure mercury lamp.

27. (Original) A process according to claim 22, wherein the second heat treatment is conducted by furnace annealing using an electrical heating furnace.

28. (Original) A process according to claim 22, wherein the rare gas element is one or more selected from He, Ne, Ar, Kr and Xe.

29. (Currently Amended) A process according to claim 22, wherein the element for promoting crystallization is one or more selected from Fe, Ni, Co[[.]], Ru, Rh, Pd, Os, Ir, Pt, Cu and Au.

30. (Currently Amended) A process for producing a semiconductor device, comprising:

forming a first semiconductor film having an amorphous structure;

adding an element for promoting crystallization to the first semiconductor film having the amorphous structure;

conducting a first heat treatment to form a first semiconductor film having a crystal structure;

forming a barrier layer over a surface of the first semiconductor film having the ~~amorphous~~ crystal structure;

forming a second semiconductor film over the barrier layer;

adding a rare gas element to the second semiconductor film;

conducting a second heat treatment to segregate the element for promoting crystallization into the second semiconductor film; and

removing the second semiconductor film.

31. (Currently Amended) A process according to claim 30, wherein the rare gas element is added by any one of ion implantation and [[or]] ion doping.

32. (Original) A process according to claim 30, wherein the barrier layer comprises a chemical oxide film formed using ozone water:

33. (Currently Amended) A process according to claim 30, wherein the barrier layer is formed by oxidizing the surface of the first semiconductor film having the ~~amorphous~~ crystal structure by plasma treatment.

34. (Currently Amended) A process according to claim 30, wherein the barrier layer is formed by oxidizing the surface of the first semiconductor film having the amorphous crystal structure with ozone generated by radiation of ultraviolet rays in an atmosphere containing oxygen.

35. (Original) A process according to claim 30, wherein the first heat treatment is conducted by radiation from one or more selected from a halogen lamp, a metal halide lamp, a xenon arc lamp, a carbon arc lamp, a high-pressure sodium lamp, or a high-pressure mercury lamp.

36. (Original) A process according to claim 30, wherein the first heat treatment is conducted by furnace annealing using an electrical heating furnace.

37. (Original) A process according to claim 30, wherein the second heat treatment is conducted by radiation from one or more selected from a halogen lamp, a metal halide lamp, a xenon arc lamp, a carbon arc lamp, a high-pressure sodium lamp, or a high-pressure mercury lamp.

38. (Original) A process according to claim 30, wherein the second heat treatment is conducted by furnace annealing using an electrical heating furnace.

39. (Original) A process according to claim 30, wherein the rare gas element is one or more selected from He, Ne, Ar, Kr and Xe.

40. (Currently Amended) A process according to claim 30, wherein the element for promoting crystallization is one or more selected from Fe, Ni, Co[.], Ru, Rh, Pd, Os, Ir, Pt, Cu and Au.

41. (New) A process for producing a semiconductor device, comprising:  
forming a first semiconductor film having an amorphous structure;  
adding an element for promoting crystallization to the first semiconductor film having the amorphous structure;  
conducting a first heat treatment to form a first semiconductor film having a crystal structure;  
forming a second semiconductor film over the first semiconductor film having the crystal structure, wherein a rare gas element is added to the second semiconductor film;  
conducting a second heat treatment to segregate the element for promoting crystallization into the second semiconductor film; and  
removing the second semiconductor film.

42. (New) A process according to claim 41, further comprising forming a barrier layer over a surface of the first semiconductor film having the crystal structure.